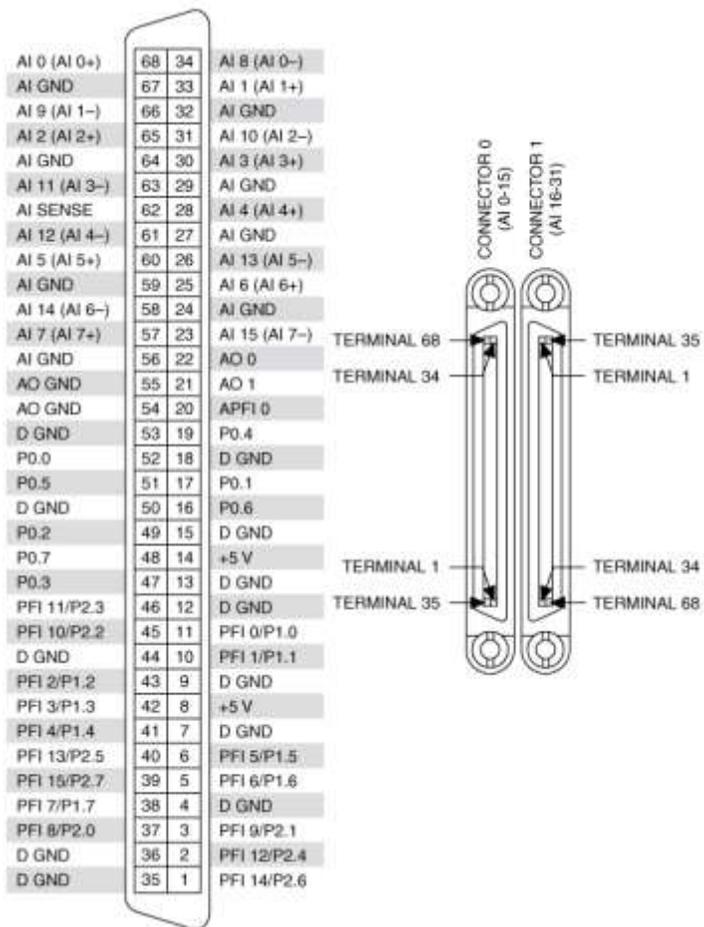


PCIe DAQ Schematics for Pair Examples





PCIe-6353 X Series



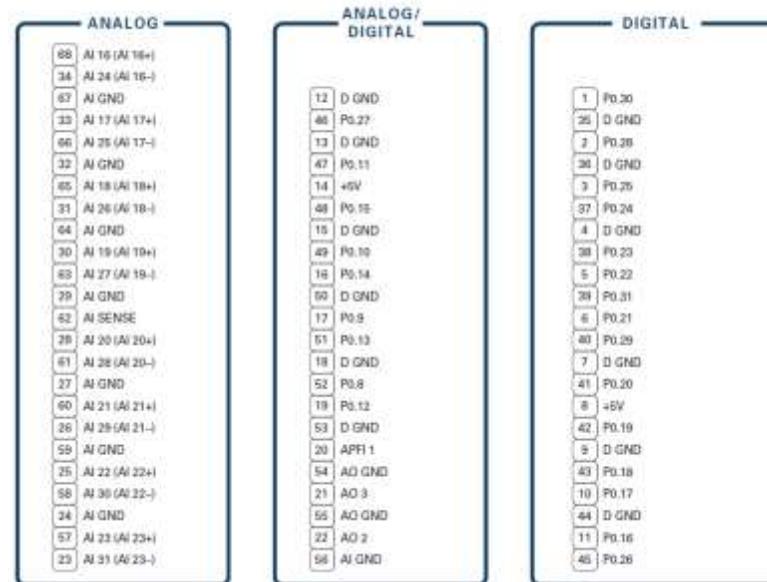
P0.30	1	35	D GND
P0.28	2	36	D GND
P0.25	3	37	P0.24
D GND	4	38	P0.23
P0.22	5	39	P0.31
P0.21	6	40	P0.29
D GND	7	41	P0.20
+5 V	8	42	P0.19
D GND	9	43	P0.18
P0.17	10	44	D GND
P0.16	11	45	P0.26
D GND	12	46	P0.27
D GND	13	47	P0.11
+5 V	14	48	P0.15
D GND	15	49	P0.10
P0.14	16	50	D GND
P0.9	17	51	P0.13
D GND	18	52	P0.8
P0.12	19	53	D GND
APFI 1	20	54	AO GND
AO 3	21	55	AO GND
AO 2	22	56	AI GND
AI 31 (AI 23-)	23	57	AI 23 (AI 23+)
AI GND	24	58	AI 30 (AI 22-)
AI 22 (AI 22+)	25	59	AI GND
AI 29 (AI 21-)	26	60	AI 21 (AI 21+)
AI GND	27	61	AI 28 (AI 20-)
AI 20 (AI 20+)	28	62	AI SENSE 2
AI GND	29	63	AI 27 (AI 19-)
AI 19 (AI 19+)	30	64	AI GND
AI 26 (AI 18-)	31	65	AI 18 (AI 18+)
AI GND	32	66	AI 25 (AI 17-)
AI 17 (AI 17+)	33	67	AI GND
AI 24 (AI 16-)	34	68	AI 16 (AI 16+)

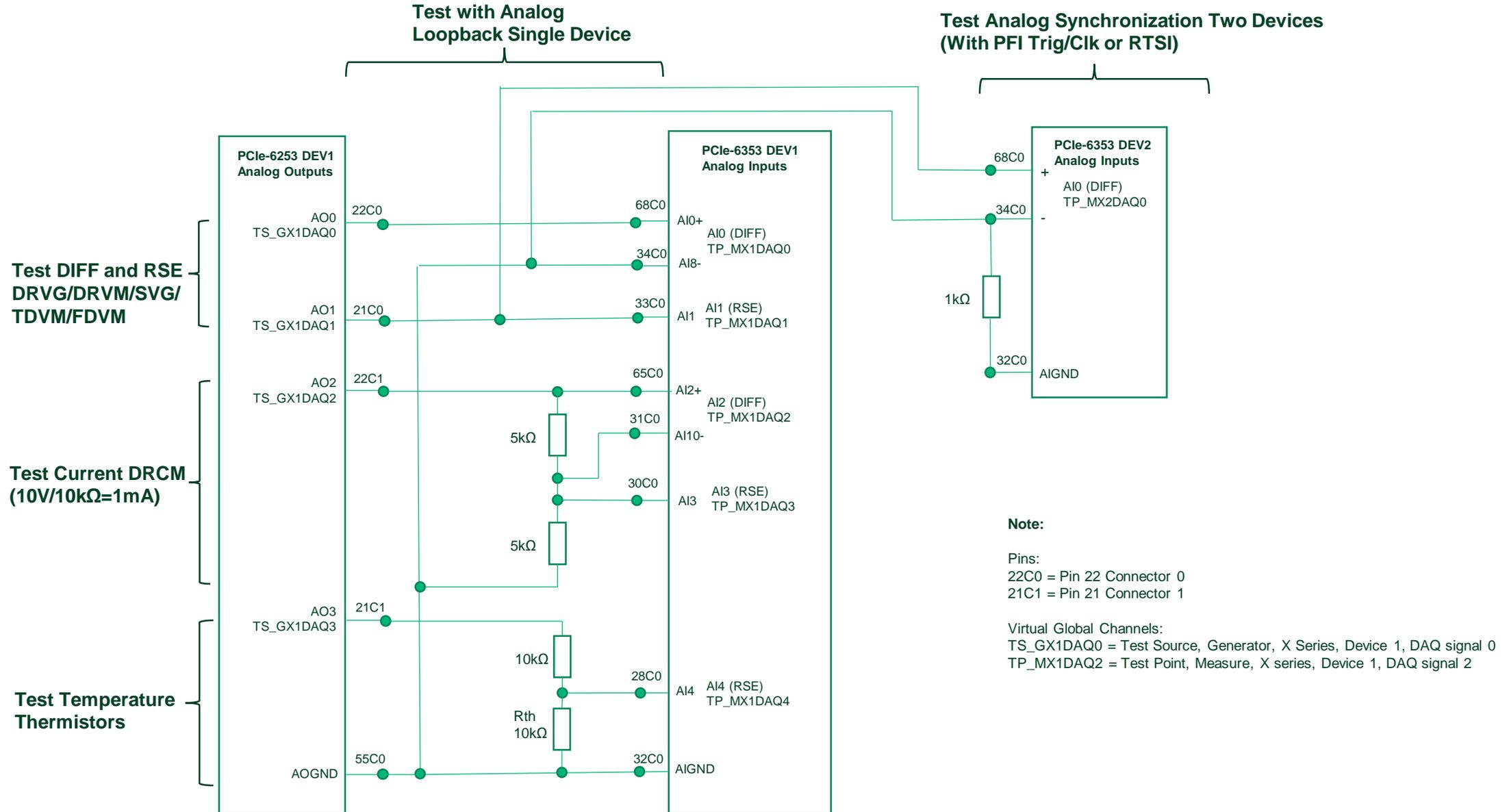
Counter/Timer Signal	Default Connector 0 Pin Number (Name)
CTR 0 SRC	37 (PFI 8)
CTR 0 GATE	3 (PFI 9)
CTR 0 AUX	45 (PFI 10)
CTR 0 OUT	2 (PFI 12)
CTR 0 A	37 (PFI 8)
CTR 0 Z	3 (PFI 9)
CTR 0 B	45 (PFI 10)
CTR 1 SRC	42 (PFI 3)
CTR 1 GATE	41 (PFI 4)
CTR 1 AUX	46 (PFI 11)
CTR 1 OUT	40 (PFI 13)
CTR 1 A	42 (PFI 3)
CTR 1 Z	41 (PFI 4)
CTR 1 B	46 (PFI 11)
CTR 2 SRC	11 (PFI 0)
CTR 2 GATE	10 (PFI 1)
CTR 2 AUX	43 (PFI 2)
CTR 2 OUT	1 (PFI 14)
CTR 2 A	11 (PFI 0)
CTR 2 Z	10 (PFI 1)
CTR 2 B	43 (PFI 2)
CTR 3 SRC	6 (PFI 5)
CTR 3 GATE	5 (PFI 6)
CTR 3 AUX	38 (PFI 7)
CTR 3 OUT	39 (PFI 15)
CTR 3 A	6 (PFI 5)
CTR 3 Z	5 (PFI 6)
CTR 3 B	38 (PFI 7)
FREQ OUT	1 (PFI 14)

NI 6251/6255/6259/6281/6289 (M Series) and NI 635x/636x/6375/6376/6378 (X Series) Devices
Connector 0 (AI 0-15)



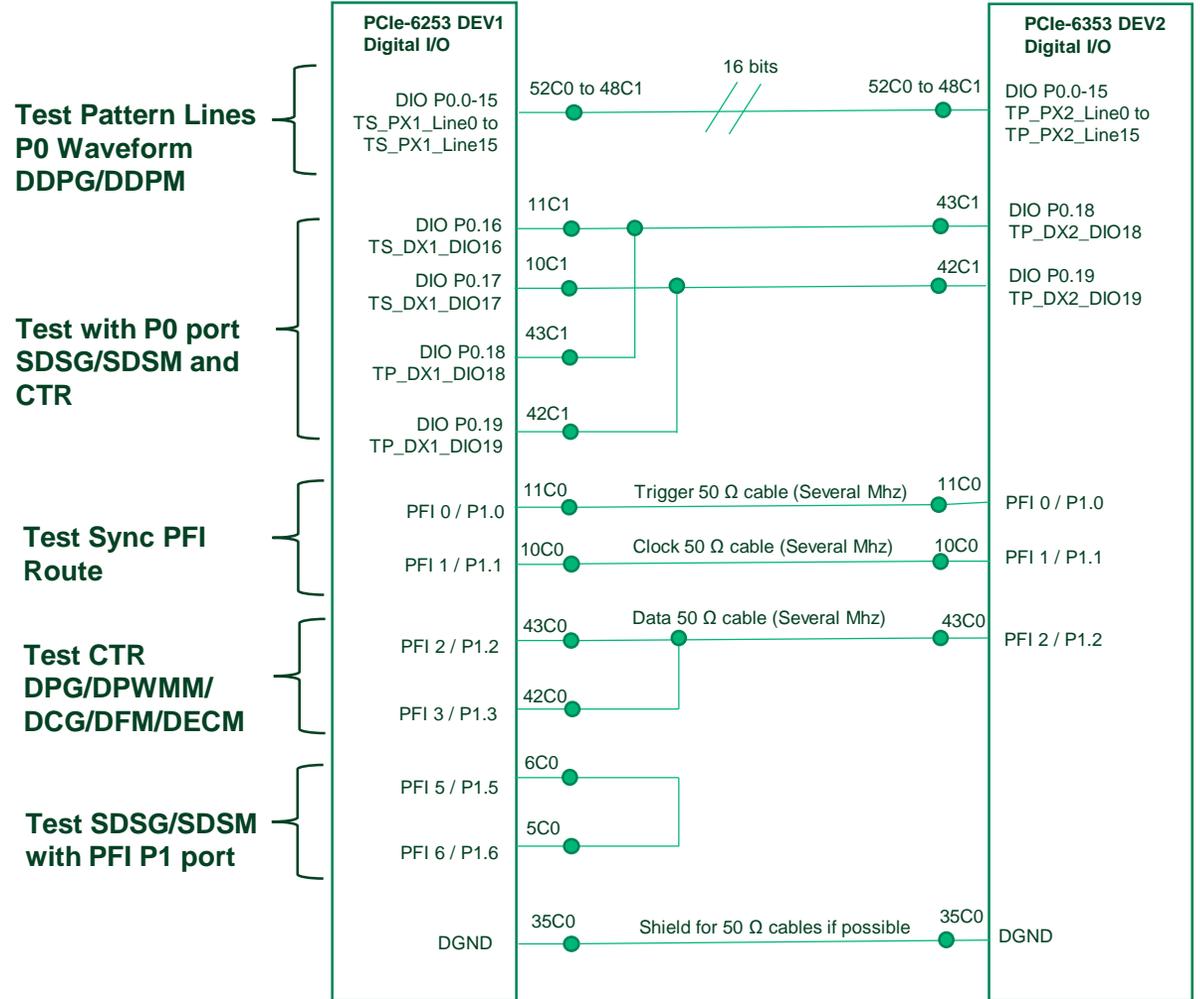
NI 6259/6289 (M Series) and NI 6353/6358/6363/6368/6378 (X Series) Devices
Connector 1 (AI 16-30)







Test Digital Synchronization two devices (With PFI Trig/clock or RTSI)



DIO P0.0-15

- | | |
|----------|-----------|
| 0 - 52C0 | 8 - 52C1 |
| 1 - 17C0 | 9 - 17C1 |
| 2 - 49C0 | 10 - 49C1 |
| 3 - 47C0 | 11 - 47C1 |
| 4 - 19C0 | 12 - 19C1 |
| 5 - 51C0 | 13 - 51C1 |
| 6 - 16C0 | 14 - 16C1 |
| 7 - 48C0 | 15 - 48C1 |

Note:

Virtual Global Channels:
 TS_PX1_Line0 = Test Source, Pattern, X Series, Device 1, Line 0
 TP_DX2_DIO19 = Test Point, Digital, X series, Device 2, DIO 19