

LF-checker: Machine Learning Acceleration of BMC for Concurrency Verification

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Vulnerabilities in multi-threaded programs

```

1  #include <pthread.h>
2  #include <assert.h>
3  int x = 0;
4  void *foo(void* arg) {
5      x++;
6      if (x>1) {
7          x--;
8      }
9      return NULL;
10 }
11
12 int main(void) {
13     pthread_t id1, id2;
14     pthread_create(&id1, 0, foo, 0);
15     pthread_create(&id2, 0, foo, 0);
16     pthread_join(id1, 0);
17     pthread_join(id2, 0);
18     assert(x == 1);
19     return 0;
20 }

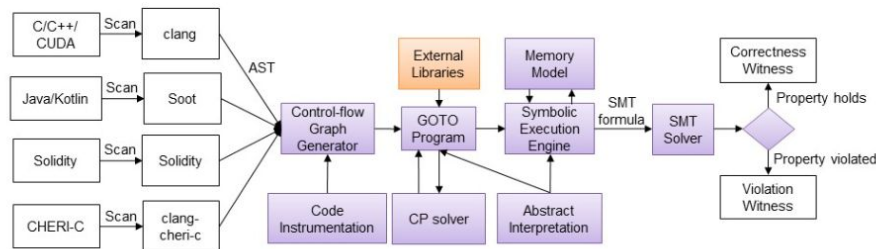
```

assert(x == 1) could **fail!**

Bounded Model Checking (BMC) — *ESBMC*

ESBMC: Software Verification Platform

Logic-based automated reasoning for checking the **safety** and **security** of single- and multi-threaded programs



workflow:

- *Control flow graph*
- *goto program convert*
- *symbolic execution*
- *SMT solving*

Combines BMC, *k*-induction, abstract interpretation, CP/SMT solving
towards correctness proof and bug hunting

www.esbmc.org

- Assume Sequential Consistency (SC).
- DFS explore all possible executions (interleavings).



```
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Limit:

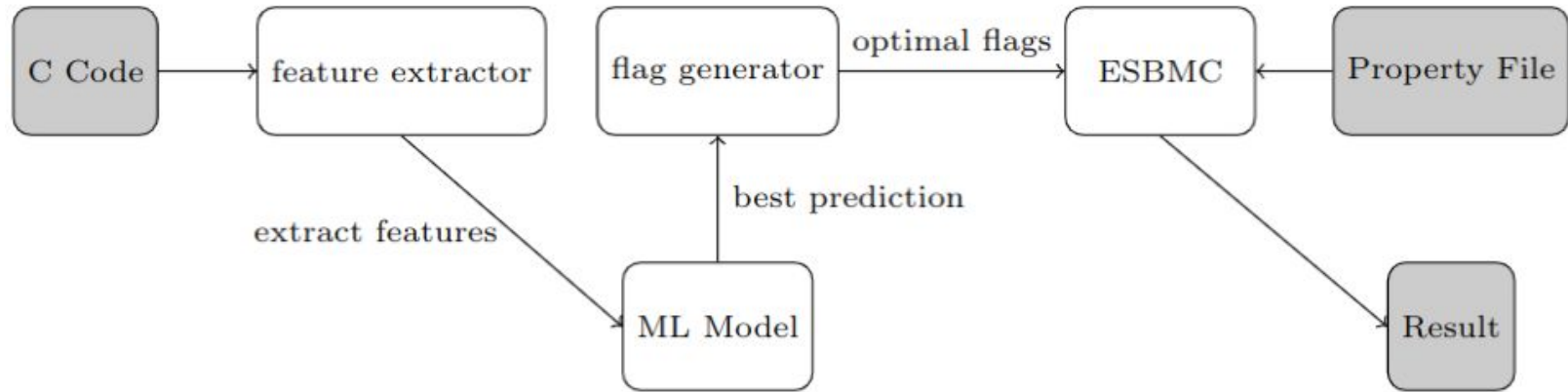
The exponentially growing thread interleavings lead the verification process hard to complete.

Summary	Table	Quantile Plot	Scatter Plot	Info										
Fixed task: <input checked="" type="checkbox"/>					ESBMC 2022-12-18 13:36:09 CET esbmc-kind.SV-COMP2...									
Click here to select columns					status	aw	scoi	witness	spect	witne	cpu (s)	wall (s)	mem (MB)	energy (J)
<input type="text"/>					TIME: <input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>
pthread-atomic/szymanski.yml true					TIMEOUT	0	view	inspect	300	380	4300	1000		
pthread-ext/02_inc_cas.yml true					TIMEOUT	0	view	inspect	300	300	800	2000		
pthread-ext/29_conditionals_vs.yml					TIMEOUT	0	view	inspect	300	300	5900	1000		
pthread-ext/39_rand_lock_p0_vs.yml					TIMEOUT	0	view	inspect	300	300	6100	3000		
pthread-ext/45_monabsex1_vs.yml					TIMEOUT	0	view	inspect	300	300	1200	3000		
pthread-ext/46_monabsex2_vs.yml					TIMEOUT	0	view	inspect	300	300	1100	2000		
ldv-linux-3.14-races/linux-3.14--drive					TIMEOUT	0	view	inspect	300	300	6900	2000		
ldv-linux-3.14-races/linux-3.14--drive					TIMEOUT	0	view	inspect	300	300	6800	1000		
ldv-linux-3.14-races/linux-3.14--drive					TIMEOUT	0	view	inspect	300	300	7100	7900		
pthread-complex/bounded_buffer.yml					TIMEOUT	0	view	inspect	300	300	740	3800		
pthread-complex/elimination_backof					TIMEOUT	0	view	inspect	300	300	4100	1000		
pthread-complex/safestack_relacy.y					TIMEOUT	0	view	inspect	300	300	1300	7800		
goblint-regression/28-race_reach_0					TIMEOUT	0	view	inspect	300	300	450	2000		
goblint-regression/28-race_reach_0					TIMEOUT	0	view	inspect	300	300	460	2000		
goblint-regression/28-race_reach_0					TIMEOUT	0	view	inspect	300	300	460	3200		
goblint-regression/28-race_reach_0					TIMEOUT	0	view	inspect	300	300	450	2000		
goblint-regression/28-race_reach_0					TIMEOUT	0	view	inspect	300	300	450	2000		
goblint-regression/28-race_reach_1					TIMEOUT	0	view	inspect	300	300	440	3000		
goblint-regression/28-race_reach_1					TIMEOUT	0	view	inspect	300	300	450	3000		
goblint-regression/28-race_reach_2					TIMEOUT	0	view	inspect	300	300	460	1000		
goblint-regression/28-race_reach_2					TIMEOUT	0	view	inspect	300	300	460	3000		
goblint-regression/28-race_reach_2					TIMEOUT	0	view	inspect	300	300	420	3000		
goblint-regression/28-race_reach_2					TIMEOUT	0	view	inspect	300	300	420	2000		
goblint-regression/28-race_reach_2					TIMEOUT	0	view	inspect	300	300	370	2000		
goblint-regression/28-race_reach_2					TIMEOUT	0	view	inspect	300	300	370	3000		

Motivation

- There are different settings in ESBMC for concurrency verification.
- Different combinations of the settings have different overall performance.
- loop unwinding bound
- context switch bound
- partial order reduction
- ...

Workflow of LF-checker



Stage 1: Verification Engine

- ESBMC exposes a large number of flags that regulate its verification strategy for concurrent program, we list them with their default types and values:

<i>ESBMC Flags</i>	<i>Type</i>	<i>Default</i>
context-bound	Integer	unlimited
strategy	String	None
k-step	Integer	1
unwind	Integer	unlimited
no-por	Boolean	disabled
no-goto-merge	Boolean	disabled
state-hashing	Boolean	disabled
add-symex-value-sets	Boolean	disabled

Stage 2: Feature Engineering

- We compute the abstract syntax tree (**AST**) of the program, and extract all possible verification features:

<i>Program Features</i>
threads created
threads joined
mutex locks
atomic locks
global variables access
global function called
binary operators
nondet variables
min. global variable access times
min. global function called times
iteration times in loop

Stage 3: Training Set Labeling

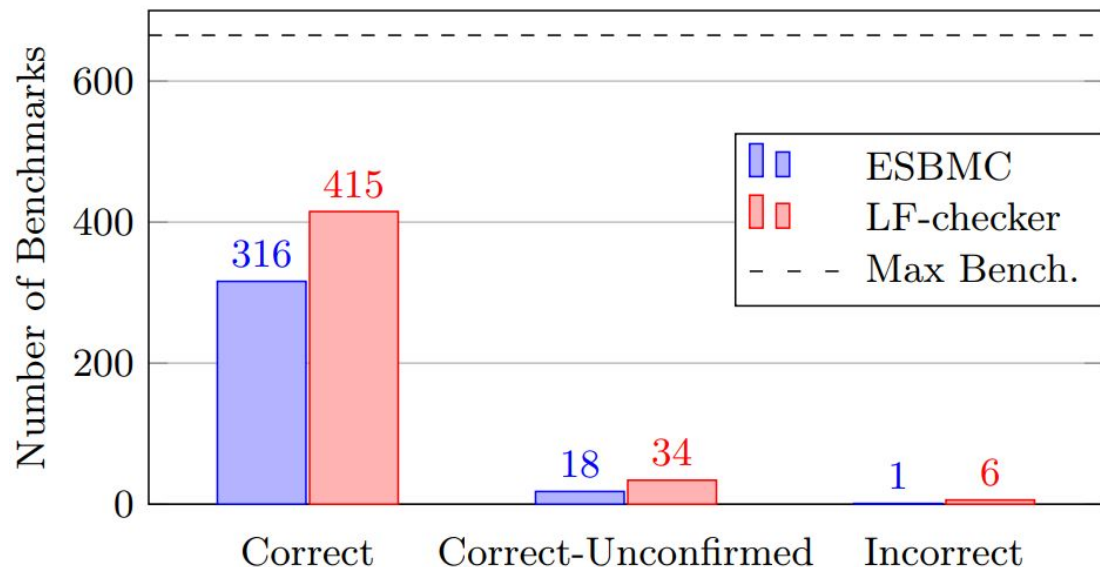
- We use 20% of the benchmarks in SV-COMP (**153** in total) to form train sets.
- Run ESBMC for **3** mins with **240** different combinations of flags.
- Summarise training samples of **36720** with labels ranging from **0** to **5**:

<i>Verification Result</i>	<i>Class</i>
correct & time ≤ 10 (s)	0
correct & $10 \text{ (s)} < \text{time} < 60 \text{ (s)}$	1
correct & time ≥ 60 (s)	2
unknown	3
timeout	4
incorrect	5

Stage 4: Decision Tree Model

- Decision Tree learns to predict the 0-5 output class given the features of the program and a given choice of flags.
- We try 240 flag combinations and pick the one that yields the lowest output class.

LF-Checker in SV-COMP 2023



We found **context bounds** and **guards merge methods** are key factors affecting the verification

Conclusion: ESBMC can output 34% more correct results compared to using its default settings.

Limitations

- 1) The model is only trained on one concurrency category (*unreach-call.ConcurrencySafety*).
- 2) The model can have better performance after fixing some issues in ESBMC (e.g., guards generation).

Reference

Tong Wu et al. *LF-checker: Machine Learning Acceleration of Bounded Model Checking for Concurrency Verification (Competition Contribution)*. 2023. doi: 10.48550 / ARXIV . 2301 . 09142. url: <https://arxiv.org/abs/2301.09142.6>

Thank you